EPISTAR

ES-VMSOPN20R

AIGaInP PN-series LED Chip

> Mechanical Specification:

(1) Dimension

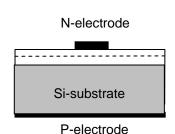
- Chip size: 20 mil x 20 mil (500±25 μm x 500±25 μm)

- Thickness: 8.8 mil (225 \pm 25 μ m) - N bonding pad: 4.5 mil (115 \pm 10 μ m)

(2) Metallization

Topside N electrode: Au alloyBackside P electrode: Au alloy





Features:

- · Long-term supply
- · High luminous intensity
- · Thin film structure
- · Vertical electrode
- · High driving current

Applications:

Automotive

> Electro-optical Characteristics at 25°C:

Parameter	Symbol		Condition	Min.	Тур.	Max.	Unit
Famuurd Valtaga	Vf1		If = 10μA	1.3	-	-	V
Forward Voltage	Vf2		If = 150mA	-	2.3	2.8	V
Reverse Current	Ir		Vr = 25V	-	-	5.0	μΑ
Peak Wavelength	λр		If = 150mA	-	621	-	nm
Dominant Wavelength ⁽¹⁾	λd		If = 150mA	610	615	620	nm
Spectra Half-width	Δλ		If = 150mA	-	18	-	nm
Luminous Intensity ⁽²⁾⁽³⁾	lv	H11	If = 150mA	6300	-	-	
		H12		7500	-	-	mcd
		H13		9000	-	-	

Note:

⁽¹⁾ Basically, the wavelength span is 10nm; however, customers' special requirements are also welcome.

⁽²⁾ Customers' special requirements are also welcome.

⁽³⁾ Luminous intensity is measured by EPISTAR's equipment on bare chips.

> Absolute Maximum Ratings⁽¹⁾:

Parameter	Symbol	Condition	Rating	Unit
Forward DC Current	If	Ta = 25°C	≤ 200	mA
Reverse Voltage	Vr	Ta = 25°C	≤ 10	V
Junction Temperature	Тј	-	≤ 125	°C
ESD withstand voltage(HBM) ⁽²⁾	V _{ESD}		Up to 2	kV
		Chip	-40 ~ +85	°C
Storage Temperature	Tstg	Chip-on-tape/storage	5 ~ 35	°C
		Chip-on-tape/transportation	-20 ~ +65	°C
Temperature during Packaging	-	-	280(<10sec)	°C

Note: (1)Maximum ratings are package dependent. The above maximum ratings were determined using a Metal Core Printed Circuit Board (MCPCB) without an encapsulant.

Stresses in excess of the absolute maximum ratings such as forward current and junction temperature may cause damage to the LED.

(2)According to ANSI/ESDA/JEDEC JS-001

> Characteristic Curves:

Fig.1 - Relative luminous Intensity vs. Forward Current

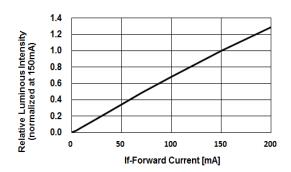


Fig.3 – Relative Intensity (@150mA) vs. Ambient Temperature

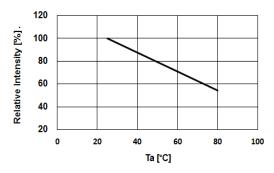


Fig.5 – Dominant Wavelength (@150mA) vs. Ambient Temperature

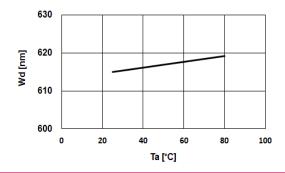


Fig.2 - Forward Current vs. Forward Voltage

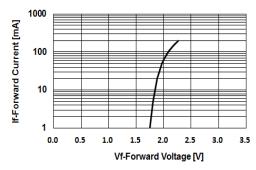


Fig.4 – Forward Voltage (@150mA) vs. Ambient Temperature

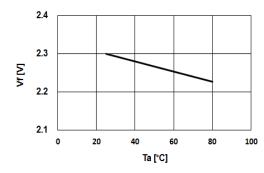
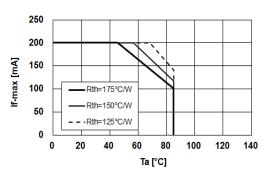


Fig.6 – Maximum Driving Forward DC Current vs. Ambient Temperature (De-rating based on Tj max. = 125°C)



> Qualification:

> Revision:

Version	Page	Subjects	Date of Modification
А		Initial Release	Mar. 2017

¹⁾ EPISTAR's LED chips and epi-wafers are designed and manufactured according to the quality management system that complies to the ISO/TS 16949:2009 requirements (IATF No: 0194279/ Certificate Registration No: 20000908 TS09).

²⁾ The chip qualification test plan is based on the guidelines of AEC-Q101-REV-D , Failure Mechanism Based Stress Test Qualification for Discrete Semiconductors in Automotive Applications.