# **EPISTAR**

### **ES-VMHRPN20R**

AIGaInP PN-series LED Chip

# > Mechanical Specification:

#### (1) Dimension

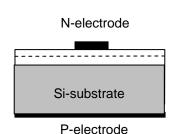
- Chip size: 20 mil x 20 mil (500 $\pm$ 25  $\mu$ m x 500 $\pm$ 25  $\mu$ m)

- Thickness: 8.8 mil (225±25 μm) - N bonding pad:  $4.5 \text{ mil} (115\pm10 \mu\text{m})$ 

#### (2) Metallization

- Topside N electrode: Au alloy - Backside P electrode: Au alloy





#### **Features:**

- · Long-term supply
- · High luminous intensity
- · Thin film structure
- · Vertical electrode
- · High driving current

#### **Applications:**

Automotive

## > Electro-optical Characteristics at 25°C:

| Parameter                            | Symbol |     | Condition  | Min. | Тур. | Max. | Unit |
|--------------------------------------|--------|-----|------------|------|------|------|------|
| Forward Voltage                      | Vf1    |     | If = 10μA  | 1.3  | -    | -    | V    |
|                                      | Vf2    |     | If = 150mA | -    | 2.3  | 2.8  | V    |
| Reverse Current                      | Ir     |     | Vr = 25V   | -    | -    | 5.0  | μΑ   |
| Peak Wavelength                      | λр     |     | If = 150mA | -    | 631  | -    | nm   |
| Dominant Wavelength <sup>(1)</sup>   | λd     |     | If = 150mA | 619  | 624  | 629  | nm   |
| Spectra Half-width                   | Δλ     |     | If = 150mA | -    | 20   | -    | nm   |
| Luminous Intensity <sup>(2)(3)</sup> | lv     | H10 | If = 150mA | 5300 | -    | -    | mcd  |
|                                      |        | H11 |            | 6300 | -    | -    |      |
|                                      |        | H12 |            | 7500 | -    | -    |      |

Note:
(1) Basically, the wavelength span is 10nm; however, customers' special requirements are also welcome.

<sup>(2)</sup> Customers' special requirements are also welcome.

<sup>(3)</sup> Luminous intensity is measured by EPISTAR's equipment on bare chips.

# > Absolute Maximum Ratings<sup>(1)</sup>:

| Parameter                                 | Symbol           | Condition                   | Rating      | Unit |
|---|------------------|-----------------------------|-------------|------|
| Forward DC Current                        | If               | Ta = 25°C                   | ≤ 200       | mA   |
| Reverse Voltage                           | Vr               | Ta = 25°C                   | ≤ 10        | V    |
| Junction Temperature                      | Tj               | -                           | ≤ 125       | °C   |
| ESD withstand voltage(HBM) <sup>(2)</sup> | V <sub>ESD</sub> |                             | Up to 2     | kV   |
| Storage Temperature                       | Tstg             | Chip                        | -40 ~ +85   | °C   |
|   |                  | Chip-on-tape/storage        | 5 ~ 35      | °C   |
|   |                  | Chip-on-tape/transportation | -20 ~ +65   | °C   |
| Temperature during Packaging              | -                | -                           | 280(<10sec) | °C   |

Note: (1)Maximum ratings are package dependent. The above maximum ratings were determined using a Metal Core Printed Circuit Board (MCPCB) without an encapsulant.

Stresses in excess of the absolute maximum ratings such as forward current and junction temperature may cause damage to the LED.

(2)According to ANSI/ESDA/JEDEC JS-001

#### > Characteristic Curves:

Fig.1 – Relative luminous Intensity vs. Forward Current

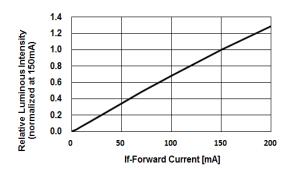


Fig.3 – Relative Intensity (@150mA) vs. Ambient Temperature

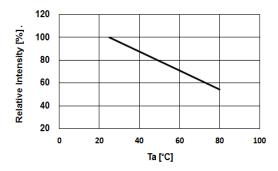


Fig.5 – Dominant Wavelength (@150mA) vs. Ambient Temperature

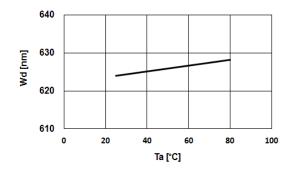


Fig.2 – Forward Current vs. Forward Voltage

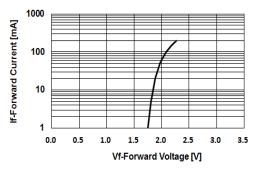
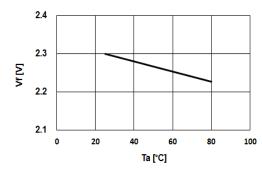
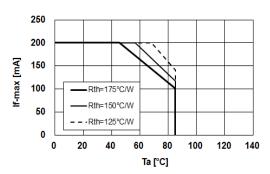


Fig.4 – Forward Voltage (@150mA) vs. Ambient Temperature



**Fig.6** – Maximum Driving Forward DC Current vs. Ambient Temperature (De-rating based on Tj max. = 125°C)



# > Qualification:

#### > Revision:

| Version | Page | Subjects   | Date of Modification |
|---------|------|--|----------------------|
| А       |      | Initial Release                                    | Mar. 2017            |
| В       | 3    | ISO/TS 16949:2009<br>Revision (IATF<br>16949:2016) | October. 2018        |
| С       | 1    | Add Iv rank H12                                    | Feb. 2020            |

<sup>&</sup>lt;sup>1)</sup> EPISTAR's LED chips and epi-wafers are designed and manufactured according to the quality management system that complies to the IATF 16949:2016 requirements (IATF No: 0325277/ Certificate Registration No: 20000910 IATF16).

<sup>&</sup>lt;sup>2)</sup>The chip qualification test plan is based on the guidelines of AEC-Q101-REV-D, Failure Mechanism Based Stress Test Qualification for Discrete Semiconductors in Automotive Applications.